

# Characterization of deep level traps responsible for isolation of proton implanted GaAs

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Deep level transient spectroscopy was employed to determine the electrical properties of defects induced in metalorganic chemical-vapor deposition grown *n*-type and *p*-type GaAs during proton bombardment. Thermal stability of these defects was investigated and correlation with defects responsible for isolation of GaAs by ion bombardment was discussed. The annealing temperature region (220–250 °C) is similar to proton isolated GaAs below the threshold dose for complete isolation. At least four of the five traps observed in *n*-type GaAs are not simple interstitial-vacancy pairs. For *p*-type GaAs we have observed an unknown level with apparent energy of  $\sim 0.64$  eV.

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## I. INTRODUCTION

Ion implantation is an essential process for production of modern III–V compound semiconductor devices and circuits and has been proven as a successful method to convert a conductive layer into a highly resistive one.<sup>1,2</sup> Due to its simplicity, precise depth control and compatibility with planar technologies, ion implantation is a potential alternative for mesa etching. Selective masking of the semiconductor surface with photoresist followed by ion irradiation is an efficient and practical way to isolate closely spaced devices.<sup>3</sup> Irradiation of GaAs by ions results in resistivity increase as a consequence of free carrier removal by trapping at defects introduced by the irradiation.<sup>4</sup> Ion beam isolation is often employed in the processing of GaAs devices, using light ions such as proton, helium, etc. Proton irradiation has been used successfully to isolate GaAs field effect transistors, high electron mobility transistors and heterojunction bipolar transistors.<sup>4</sup> Waveguiding regions in optoelectronic devices were fabricated by controllable introduction of defects using proton bombardment.<sup>5</sup> Thus, the understanding of defect creation, carrier trapping and thermal stability of the traps is essential in achieving better device performance.

The threshold dose to convert a conductive GaAs layer to a highly resistive one was found to closely correlate with the estimated number of lattice atom replacements along the depth of the doped layer.<sup>6</sup> The threshold doses are quite similar for irradiation conducted at room temperature or at 220 °C,<sup>7</sup> in spite of the enhanced dynamic annealing in the later case. Antisite defects originating at the replacement collisions and their related defect complexes are considered to be the carrier trapping centers,<sup>6</sup> by virtue of their low sensitivity to dynamic annealing. The stability of the isolation during postirradiation thermal annealing increases progres-

sively with the increase of the implanted dose and for a constant dose a higher stability of the isolation occurs in samples having the lower original sheet carrier concentration.<sup>8</sup>

Isolation of *n*-type and *p*-type GaAs layers of similar original sheet carrier concentration is attained after implantation of identical ion doses.<sup>9</sup> This result was explained assuming that conduction electrons and holes are trapped by acceptor and donor centers, these centers being related to Ga and As antisite complex defects, respectively. Because the displacement energies of Ga and As are almost equivalent,<sup>10</sup> similar concentrations of both antisite defects are formed in the collision cascades and hence similar concentrations of electron and hole traps are created.

Electron irradiation damage in GaAs has been studied extensively using a variety of techniques. Electrical resistivity, Hall, and *C*–*V* measurements have been used to study the production of damage at different energies and the annealing kinetics of the defects.<sup>11,12</sup> Deep level transient spectroscopy (DLTS) measurements have been widely employed,<sup>13,14</sup> and their use has led to a fairly detailed model of many of the traps introduced into *n*- and *p*-type GaAs by electron irradiation.<sup>15,16</sup> The traps identified in proton irradiated GaAs<sup>17</sup> include most of those, found in electron irradiated materials, either after irradiation or after irradiation and annealing. Some of these traps, which have not been observed in electron irradiated GaAs, are associated with defects which are more complex than simple interstitial-vacancy pairs.

In the present work, the electrical properties of defects induced in metalorganic chemical-vapor deposition grown *n*-type and *p*-type GaAs during proton bombardment were investigated. DLTS measurements were used in an attempt to correlate the deep level center annealing process with the thermal stability of *n*-type and *p*-type GaAs, isolated by proton irradiation. The deep levels measured in this work were

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compared with electron bombardment induced deep levels, published previously.

## II. EXPERIMENTAL TECHNIQUES

Epitaxial layers of *n*-type and *p*-type GaAs were grown on epi-ready vertical gradient freeze Si-doped (carrier concentration of  $2 \times 10^{18} \text{ cm}^{-3}$ ) and Zn-doped (carrier concentration of  $6\text{--}8 \times 10^{18} \text{ cm}^{-3}$ ) (100) GaAs with  $2^\circ$  off normal toward the (110) using the Australian National University (ANU) metalorganic chemical-vapor deposition (MOCVD) reactor. Trimethylgallium was used as the group III source with  $\text{H}_2$  as the carrier gas and arsine was used as the group V source. Epitaxial layers of 6- $\mu\text{m}$ -thick GaAs were grown to produce *n/n*<sup>+</sup> and *p/p*<sup>+</sup> samples. Electron concentration of  $1.0 \times 10^{16} \text{ cm}^{-3}$  in the *n*-type samples and hole concentration of  $1.1 \times 10^{16} \text{ cm}^{-3}$  in *p*-type samples were obtained by using diluted silane and dimethylzinc, respectively. Irradiation was carried out using the ANU 1.7 MV tandem accelerator with 600 keV  $\text{H}^+$  ions from TiH source. During irradiation, the samples were held nominally at room temperature and tilted  $7^\circ$  from the beam axis to minimize channeling effects. The ion fluence was  $1 \times 10^{11} \text{ cm}^{-2}$  with a constant flux of  $\sim 0.5 \text{ nA/cm}^2$ . After irradiation, the samples were submitted to rapid thermal annealing (RTA) for 1 min in the temperature range 100–600 °C in Ar atmosphere. The annealing steps were performed with the irradiated face in close proximity with fresh GaAs dummy wafer to prevent excessive loss of As from the surface. All samples were then chemically cleaned, including a final dip in diluted hydrochloric acid to remove any surface oxide. Immediately after cleaning, the samples were loaded into a vacuum chamber to form Schottky barrier contacts by thermal evaporation of Au at a base pressure of  $\sim 2 \times 10^{-6}$  Torr. The deposited film thickness was  $\sim 100 \text{ nm}$ . *C*–*V* and DLTS measurements were carried out in experimental setup which consists of a 1 MHz high-speed capacitance bridge, a pulse generator, and a cryostat (77–373 K). The setup is controlled via computer interface, except for the temperature regulator, which is operated manually. The measured transients are transferred to and stored in the memory of the computer together with the sample temperature. The apparent energy level ( $E_a$ ) in the band gap and apparent capture cross section ( $\sigma_a$ ) of defects were determined from Arrhenius plots of  $\tau T^2$  vs  $1/T$ , where  $1/\tau$  is the emission rate at a temperature  $T$ .

## III. RESULTS AND DISCUSSION

Figure 1 shows the DLTS spectra for as-grown, irradiated and various temperature annealed *n*-type [Fig. 1(a)] and *p*-type [Fig. 1(b)] samples for a rate window of  $0.98 \text{ s}^{-1}$ . Five main peaks were observed for *n*-type (called *Pn1*, *Pn2*, *Pn3*, *Pn4*, and *Pn5*) and four for *p*-type samples (*Pp1*, *Pp2*, *Pp3*, and *Pp4*) in the scanned temperature interval (77–373 K). We have shown in Fig. 2 the fitting of DLTS spectra with a sum of a number of Lorentzian peaks. Using this technique we have separated the peaks better, especially for the case of a small peak overlapping with a large one [see Fig. 2(a)]. This method permits one to obtain the maximum temperatures ( $T_{\text{max}}$ ) of the peaks more precisely.

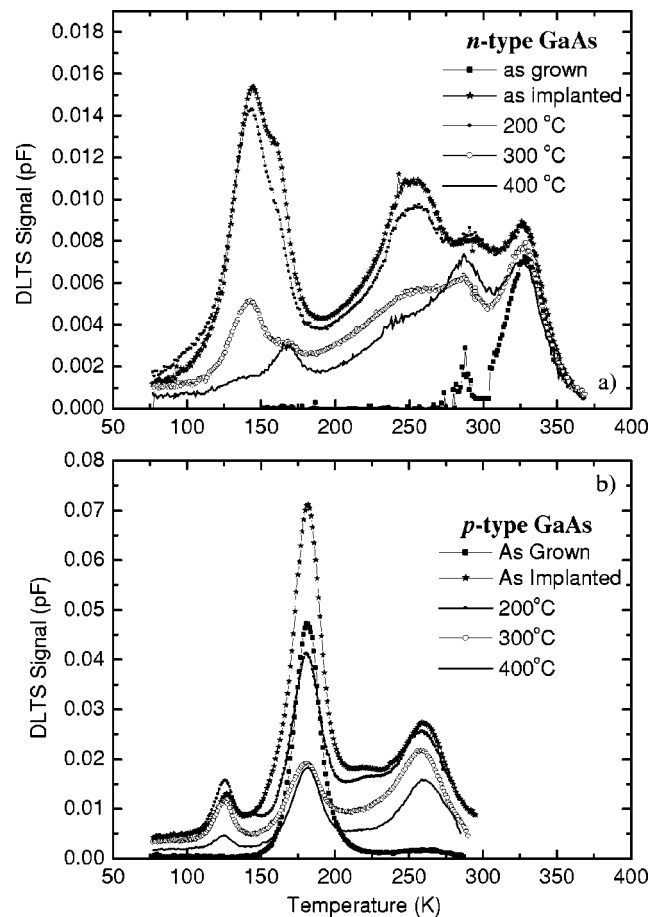


FIG. 1. DLTS spectra (rate window  $0.98 \text{ s}^{-1}$ ) showing the evolution of the deep levels with the annealing temperature after  $\text{H}^+$  implantation to a dose of  $1 \times 10^{11} \text{ cm}^{-2}$  for *n*-type (a) and *p*-type (b) GaAs.

The trap signatures of the observed deep levels (experimental points) are compared with those of known deep level centers (solid lines) as shown by the plot in Fig. 3. The signature of *Pn1* is similar to that of *EL2* level. There are presently two microscopic models of *EL2*. One is that *EL2* is an intrinsic and complex lattice defect with an arsenic antisite  $\text{As}_{\text{Ga}}$  as a core and arsenic interstitial  $\text{As}_i$  as a neighbor, forming pair as a result of a Coulomb interaction.<sup>18</sup> The other is that *EL2* is simply an isolated arsenic antisite  $\text{As}_{\text{Ga}}$  or gallium vacancy-arsenic interstitial ( $\text{V}_{\text{Ga}}\text{As}_i$ ) complex.<sup>19</sup>

*Pn2* and *Pn3* peaks were resolved, using the method shown in Fig. 2. From the trap signature of *Pn2*, it is likely that it corresponds to two different peaks with similar apparent energy. We can note this peak in as-grown material [see Fig. 1(a)]. It is known like *E* level<sup>18</sup> and was measured in as-grown MOCVD material. After irradiation and during post-irradiation annealing *Pn2* is transformed into the *P3* level well known from the literature.<sup>14</sup>

Many works claim that the well known traps *E1*–*E5*<sup>14</sup> in *n*-type GaAs have to be ascribed to simple defects, i.e., that they are produced by the displacements of one single atom with a threshold energy of  $\sim 10 \text{ eV}$ . We would like to mention here that the threshold energy for the replacement collision to occur is the same. Consequently, during a knock on with transferred energy of 10–15 eV, it is possible to create

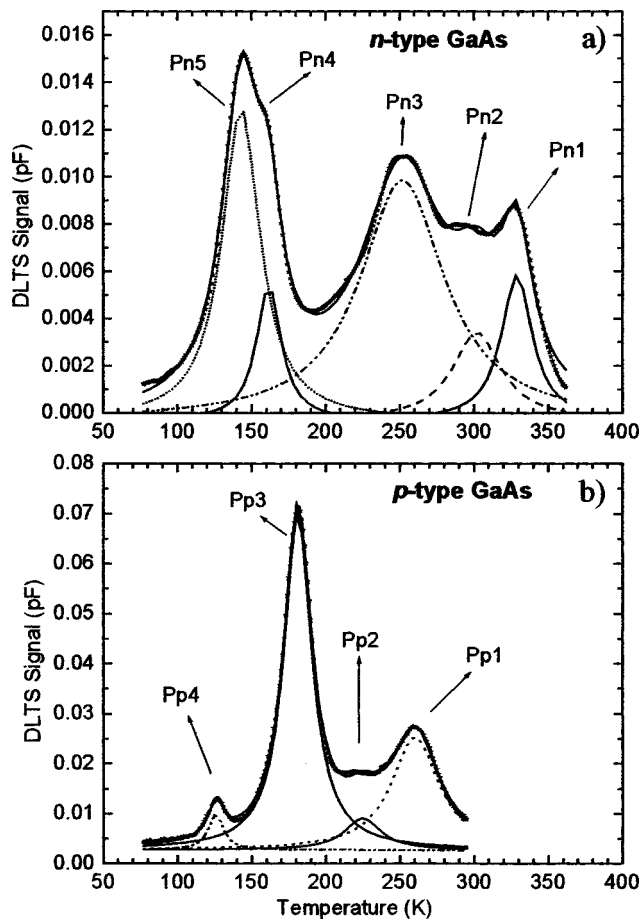


FIG. 2. Fitting of DLTS spectra with a sum of Lorentzian peaks for *n*-type (a) and *p*-type (b) GaAs.

not only simple defects (like  $V_{As}$ ,  $V_{Ga}$ ,  $Ga_i$ ,  $As_i$ ), but also antisite related defect complexes (for example,  $As_{Ga} + V_{As} + Ga_i$  or  $Ga_{As} + V_{Ga} + As_i$ ). Taking into account the different mobility for different parts of these defects at the temperature of bombardment and even the ion irradiation current density influence, we can explain the creation of various complex defects. For example the peak *Pn3* in Fig. 3 has a signature of E4 defect,<sup>20</sup> which is believed to be an  $As_{Ga} + V_{As}$  defect complex.<sup>15</sup> The small *Pn4* peak at  $\sim 170$  K has a trap signature similar to that of *P1*,<sup>14</sup> which must be a complex defect, because its concentration increases with the annealing temperature [see Figs. 1(a) and 4(a)]. The last peak *Pn5* has a signature close to that of E3, which is believed to be a simple defect,<sup>16</sup> but it was shown<sup>17</sup> that this peak is due to two different traps, one of them being E3 and the other a complex defect. All the observed deep levels in *n*-type GaAs are listed in Table I.

The main difference between electron and proton irradiated *n*-type GaAs is that in the later case more complex defects are observed (eventually antisite related), with respect to the simple defects in the former case. There is one more issue, related to traps and isolation of GaAs, which we would like to discuss. To the best of our knowledge, there is no published DLTS result discussing the Ga antisite as a possible trap, even though electrically  $Ga_{As}$  is a good candidate for trapping electrons. The common explanation in the

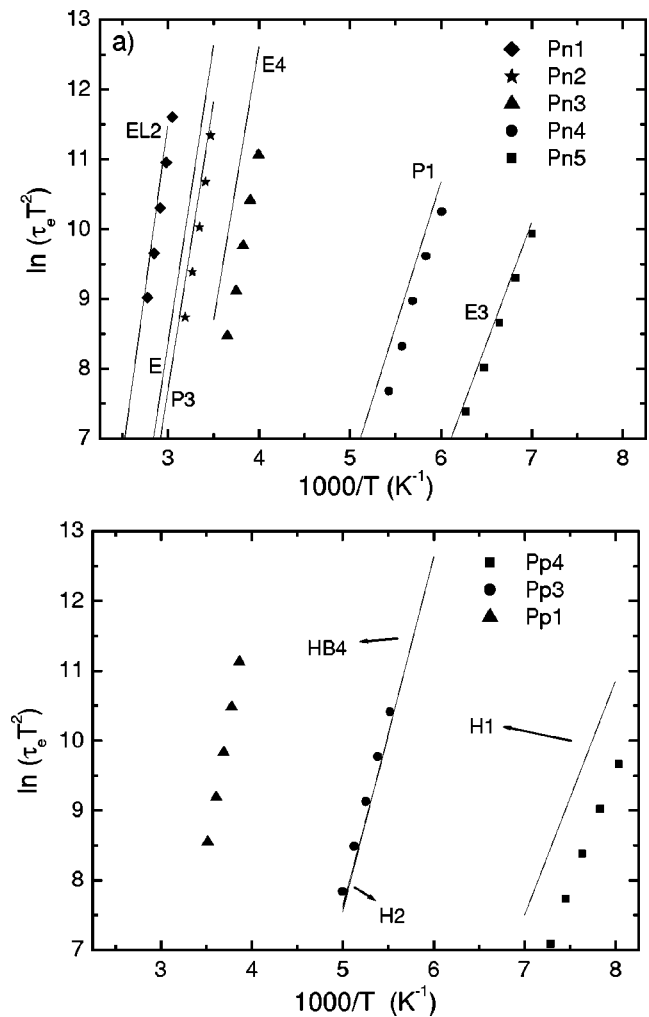


FIG. 3. Comparison of the trap signatures for the observed deep levels (experimental points) and those from the literature (solid lines) for *n*-type (a) and *p*-type (b) GaAs (see Tables I and II).

literature is that due to the high mobility of the point defects in the Ga sublattice during irradiation, these defects are not stable. This is true for  $V_{Ga}$  and  $Ga_i$ , but the  $Ga_{As}$  like a point defect without the presence of other point defects in close proximity could be stable and its annealing depends on the flux of other point defects. In this way the Ga antisite defect could participate in the isolation process, too.

In *p*-type GaAs the situation is different. It is well accepted that creation of defect complexes is possible even during low dose electron irradiation.<sup>15</sup> DLTS spectra for proton irradiated *p*-type GaAs samples [Figs. 1(b) and Fig. 3(b)] have not previously been reported. The signature of *Pp1* is not known from the literature and presents characteristic energy level of  $E_T - E_V \sim 0.64$  eV. A very broad *Pp2* peak is seen around 210–230 K. It is interesting to note that this is the temperature range where the so-called U band is commonly observed in irradiated *n*-type samples.<sup>21</sup> From the trap signature of *Pp3*, it is possible to correlate this peak with two different trap levels, having similar apparent parameters. *Pp3* exists in the as-grown material [Fig. 1(b)] and corresponds to *HB4* observed in epitaxial layers<sup>18</sup> or the so-called *H2* level, observed in electron irradiated *p*-type GaAs.<sup>22</sup> *Pp4* apparent

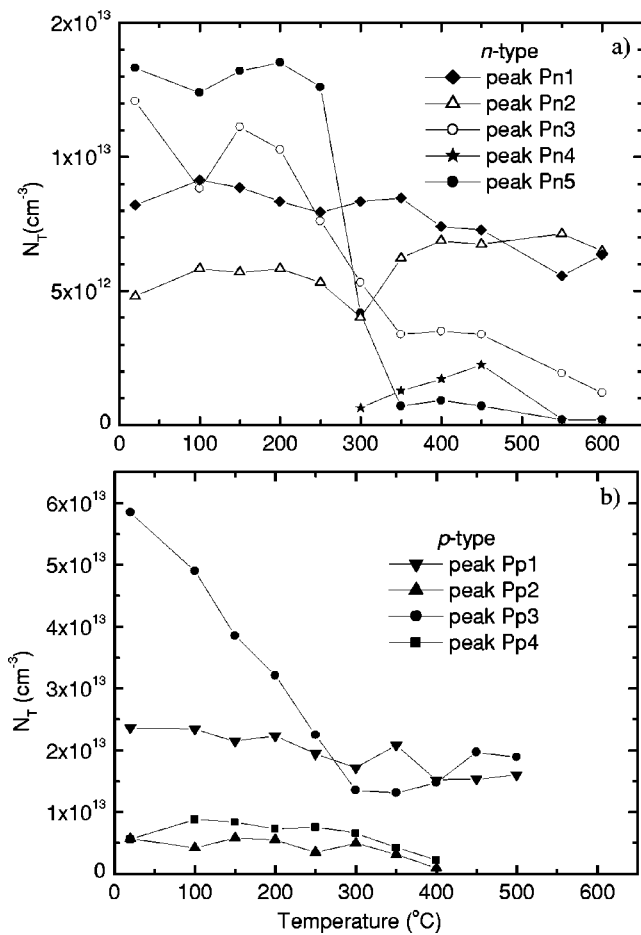


FIG. 4. Thermal stability of the measured deep levels in *n*-type (a) and *p*-type (b) GaAs after 1 min RTA annealing in Ar atmosphere.

energy is similar to  $H1^{16}$  trap level, known as a Frenkel pair defect. Table II summarizes the observed deep levels in *p*-type GaAs.

We consider now the thermal annealing of the measured defects. Figure 4 shows the dependence of the trap concentrations versus the annealing temperature. The levels *Pn3* and *Pn5* have similar annealing behavior. It is known<sup>16</sup> that *E3* and *E4* levels anneal at the same characteristic temperature (so-called stage III). The similarity in the annealing behavior between *Pn5* and *Pn3* suggests that the mentioned complex defect constituting *Pn5* has the same structure like *E4*. Maybe these two levels correspond to the same defect, but with different charge state (two different charge states of  $As_{Ga} + V_{As}$  are considered in Ref. 23). Another interesting feature observed in Fig. 4(a) is that the concentration of *Pn2* and *Pn4* (levels *P3* and *P1*, respectively) is increasing for

TABLE I. Deep traps in *n*-type GaAs.

Peak	$E_{na}$ (eV)	$\sigma_{na}$ (cm <sup>2</sup> )	Defect	Reference
<i>Pn1</i>	0.82	$2 \times 10^{-13}$	EL2	18
<i>Pn2</i>	0.80	$6 \times 10^{-12}$	P3	14
<i>Pn3</i>	0.67	$2 \times 10^{-12}$	E4	19
<i>Pn4</i>	0.39	$7 \times 10^{-14}$	P1	14
<i>Pn5</i>	0.30	$8 \times 10^{-15}$	E3	16

TABLE II. Deep traps in *p*-type GaAs.

Peak	$E_{na}$ (eV)	$\sigma_{na}$ (cm <sup>2</sup> )	Defect	Reference
<i>Pp1</i>	0.64	$3 \times 10^{-14}$	?	...
<i>Pp3</i>	0.42	$1 \times 10^{-14}$	<i>HB4, H2</i>	18,21
<i>Pp4</i>	0.29	$3 \times 10^{-14}$	<i>H1</i>	16

temperatures higher than 250 °C in agreement with the data of Pons, Mircea, and Bourgoïn.<sup>14</sup> data. The *Pn1* (*EL2*) level does not change considerably during the annealing. Figure 4(b) shows the annealing behavior of traps observed in *p*-type GaAs. All the peaks decrease slowly with the temperature, but the most significant decreasing is of the peak *Pp3* (*H2*), where for temperatures above 200 °C, the concentration of this level becomes lower than in the as-grown sample.

Figure 5 compares the temperature dependence for the sum of all *n*-type and *p*-type traps. It is worth mentioning here that we did not observe *E1*, *E2* and *H0* levels,<sup>18</sup> due to the restricted temperature interval (77–373 K) of our DLTS measurements. We observed that the main annealing temperature region is 220–250 °C (named stage III<sup>16</sup>). This region coincides with the characteristic temperature of thermal stability for proton-isolated *n*-type and *p*-type GaAs<sup>8,9</sup> with doses below the threshold dose for complete isolation. Using this correlation, we can conclude that the measured deep level defects in the present work are part of the traps responsible for the isolation in ion bombarded GaAs.

#### IV. CONCLUSIONS

The results shown in this work lead to the conclusion that not all the traps, observed in electron irradiated *n*-type and *p*-type GaAs can be identified in proton irradiated epitaxial layers. Some traps are detected in proton irradiated GaAs, which are not present in electron irradiated GaAs. For *p*-type GaAs we have observed an unknown level with apparent energy of ~0.64 eV. At least four of the five traps observed in *n*-type GaAs are not simple defects. Strong evi-

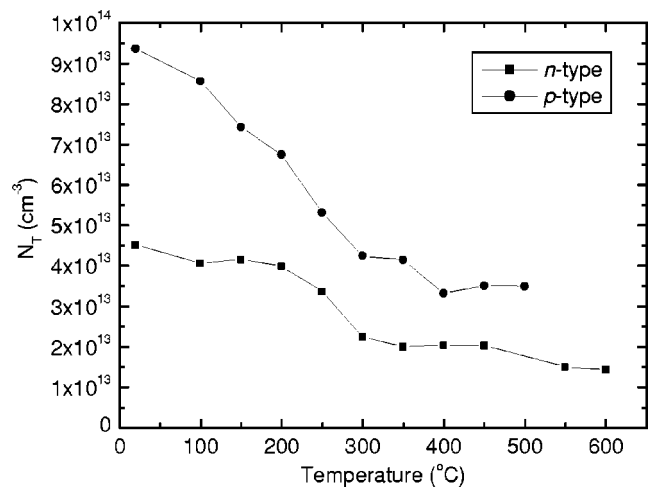


FIG. 5. Temperature evolution for the sum of all measured deep levels in *n*-type and *p*-type GaAs.

dence is found that most of the defects responsible for trapping during ion isolation are related to antisite complex defects. The characteristic annealing temperature region (220–250 °C) for these defects is similar to proton isolated GaAs below the threshold dose for complete isolation.

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